

nRF52832 base processor with DC/DC configuration

Programming and Debugging Pins

Buttons

Battery Holder

LED Matrix

Diagram showing the pin connections for headers J1 and J2:

- J1:**
 - Pin 2: PG.11/TX
 - Pin 1: PG.12/RX
- J2:**
 - Pin 4: GND
 - Pin 3: VDD P3V
 - Pin 2: SWDIO
 - Pin 1: SWDCLK

BAT-HLD-002-SMT-TR